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PATENT APPLICATION

Docket No.: 108102

LETHE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Application No.: 09/720,860

Filed: January 2, 2001

SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURE THEREOF

CIRCUIT BOARD AND ELECTRONIC INSTRUMENT

INFORMATION DISCLOSURE STATEMENT

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

For:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. References 1 & 2 were cited in a counterpart foreign application. An English language version of the foreign search report is attached for the Examiner's information.
- 3. English-language Abstracts of the non-English language references are attached hereto.

Respectfully submitted,

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JAO:JSA/zmc

Date: January 29, 2001

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